

The IEEE Workshop on Signal Processing Systems (SiPS) is a premier international forum in the area of design and implementation of signal processing systems. It addresses all aspects of architecture and design methods of these systems. Emphasis is on current and future challenges in research and development in both academia and industry.

SUBMITTING A PAPER

We invite prospective authors to submit original papers in areas including, but not limited to:

Theme of the Year: Towards Sustainable Systems

- Low energy systems
- Low environmental impact systems

Software Implementation of Signal Processing Systems

- Software on programmable digital signal processors
- Application-specific instruction-set processor (ASIP) architectures and systems
- SIMD, VLIW and multi-core CPU architectures
- GPU-based massively parallel implementations

Hardware Implementation of Signal Processing Systems

- Low-power signal processing circuits and applications
- High-performance VLSI systems
- FPGA and reconfigurable architecture-based systems
- System-on-chip and network-on-chip
- VLSI for sensor network and RF identification systems
- Processing-in-memory signal processing systems

Machine Learning for Signal Processing

- Circuits and systems for Al
- Deep learning/machine learning/Al algorithms
- Tools/platforms for Al
- Edge and cloud AI computing platforms
- Hardware/neuromorphic accelerators
- Hardware/software co-design and automation for Al

Design Methods of Signal Processing Systems

- Optimization of signal processing algorithms
- Compilers and tools for signal processing systems
- Algorithm-to-architecture transformation
- Dataflow-based design methodologies
- Error-tolerant techniques for signal processing

Signal Processing Application Systems

- Audio, speech, and language processing
- Biomedical signal processing and bioinformatics
- Image, video, and multimedia signal processingInformation forensics, security, and cryptography
- Sensing and sensor signal processing
- Signal processing for non-volatile memory systems

- Latency-and power-constrained signal processing
- Wireless communications and MIMO systems
- Coding and compression
- Signal processing for mixed-signal technologies

Special sessions:

https://sips2022.insa-rennes.fr/special-sessions/

Submission Deadline

Conference track: April 30, 2022

Journal track: April 30, 2022

COMMITTEE

General Chairs

John McAllister, QUB

Maxime Pelcat, INSA Rennes, IETR

Program Chairs

Francesca Palumbo, UNISS

Daniel Ménard, INSA Rennes, IETR

Publication Chair

Raquel Lazcano, UNISS

Communication Chairs

Hervé Yviquel, UNICAMP

Xiaodong Yu, ANL

Finance Chair

Jean-François Nezan, INSA Rennes, IETR

Special Sessions Chair

Philippe Coussy, LabSTICC

Local Organization Chair

Karol Desnos, INSA Rennes, IETR

Asian Liaison: Gwo Giun Lee, NCKU

European Liaison: Eduardo Juárez Martínez, UPM

North American Liaison: Shuvra S. Bhattacharyya, UMD,





